EISTAR Digital Logic Probe Model: LP-1



Features:

- 1- DTL/TTL Circuit Tester Pen (DC 18V Max)
- 2- Circuit powered
- 3- Compatible w/TTL & CMOS
- 4- Captures 50 ns pulses

Specifications

Input	
Overload protection	±50VDC continuous 117VAC for 15s
Impedance	300kΩ
Min. Detectable	50n
Pulse Width	10MHz
Threshold Voltages	
DTL/TTL	Low: 0.80 ±10VDC High: 2.25 ±15VDC
Pulse Detector	
Pulses	Either a rising or falling pulse edge transition is stretched into a single flash of "PULSE LED". Pulse train causes continuous LED flashing
Memory	"PULSE LED" latches on with the first transition, remains on until reset as long as probe is powered
Power Supply	
Circuit Power	5 to 18VDC
Overload	±35VDC
General	
Operating Temp.	10 to 40 degrees C
Size	1.0" (W) x 5.8" (D) x 0.7" (H)
Weight	12 oz.

Made in China